

IPW  
PATENT  
P17732  
77.0059



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of  
Schmisseur  
Serial No.: 10/747,932  
Filed: December 29, 2003

) Examiner: Mano Padmanabhan  
)  
) Art Unit: 2188  
)

For: METHOD, SYSTEM, AND PROGRAM FOR MANAGING DATA ORGANIZATION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application.

A. Non-English Language References

- ☐ Enclosed is a search report for a counterpart application. The search report Examiner has provided comments on the relevancy of any non-English language references cited in the search report.
- ☐ The specification incorporates comments on the relevancy of Non-English language references.
- ☐ Set forth below are comments provided by the applicant's home country counsel on the relevancy of non-English language references:

- B. ☒ The information disclosure statement submitted herewith is being filed
- 1) within three months of the filing date of a national application other than a continued prosecution application under 1.53(d);

- 2) within three months of the date of entry into the national stage as set forth in 1.491 in an international application;
- 3) before the mailing date of a first Office Action on the merits; or
- 4) before the mailing date of a first Office Action after filing of a Request for Continued Examination under 1.114.

Because this submission is made prior to the mailing of a first Office Action on the merits, it is believed that no fee is due. Please contact the undersigned with any questions in this regard.

Respectfully submitted,

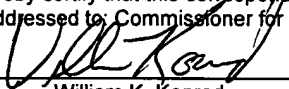


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December 1, 2005

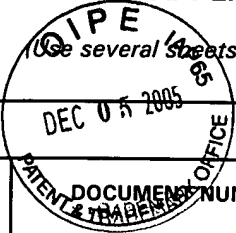
Date

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date indicated above and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



William K. Konrad

12/1/05  
(Date)

<b>FORM PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  77.59				<b>Docket Number (Optional)</b> P17732		<b>Application Number</b> 10/747,932																																																																															
 (Use several sheets if necessary)				<b>Applicant</b> Schmisser		<b>Group Art Unit</b> 2188																																																																															
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<b>EXAMINER INITIAL</b>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th colspan="8">DOCUMENT NUMBER</th> <th>DATE</th> <th>NAME</th> <th>CLASS</th> <th>SUBCLASS</th> <th>FILING DATE</th> </tr> <tr> <td></td><td>6</td><td>2</td><td>4</td><td>7</td><td>1</td><td>5</td><td>7</td> <td>6-12-01</td> <td>Edirisooriya</td> <td></td> <td></td> <td></td> </tr> <tr> <td></td><td>6</td><td>3</td><td>5</td><td>3</td><td>8</td><td>9</td><td>5</td> <td>3-5-02</td> <td>Stephenson</td> <td></td> <td></td> <td></td> </tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>							DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE		6	2	4	7	1	5	7	6-12-01	Edirisooriya					6	3	5	3	8	9	5	3-5-02	Stephenson																																										
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<b>FOREIGN PATENT DOCUMENTS</b>													
	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	Translation	
												YES	NO

<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
	US-2005-0160307 Published 7-21-05 (atty dkt 77.56)		
	Patterson, et al. "A Case for Redundant Arrays of Inexpensive Disks (RAID)," 1988 ACM 0-89791-268-3/88/0006/0109; pp. 109-116.		
	HP Technical Guide, "Mathematics behind RAID 5DP," Hewlett-Packard Company, 2001; 10 pp.		
	Intel IOP303 I/O Processor. Internet document <a href="http://developer.intel.com/design/iio/80303.htm">http://developer.intel.com/design/iio/80303.htm</a> ; copyright 2003, 4 pp. (available prior to Dec. 29, 2003)		
	Intel IOP331 I/O Processor. Internet document <a href="http://developer.intel.com/design/iio/iop331.htm">http://developer.intel.com/design/iio/iop331.htm</a> ; copyright 2003, 4 pp. (available prior to Dec. 29, 2003)		
	Intel IOP331 I/O Processor Chipset. Internet document <a href="http://developer.intel.com/design/iio/docs/iop331.htm">http://developer.intel.com/design/iio/docs/iop331.htm</a> ; copyright 2003, 1 p. (available prior to Dec. 29, 2003)		
	Intel 80333 I/O Processor, Developer's Manual. Intel Document Number 305432001US. Copyright March 2005. Copyright & contents (pp 1-40); Chapters 1 (pp 41 - 48) and 6 (pp 375 - 408). (subject matters of chapters 1 and 6 available prior to Dec. 29, 2003)		
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%;"><b>EXAMINER</b></td> <td><b>DATE CONSIDERED</b></td> </tr> </table>		<b>EXAMINER</b>	<b>DATE CONSIDERED</b>
<b>EXAMINER</b>	<b>DATE CONSIDERED</b>		

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.